

Electronic Acknowledgement Receipt

EFS ID:	6347073
Application Number:	10823729
International Application Number:	
Confirmation Number:	5344
Title of Invention:	Semiconductor device fabrication method
First Named Inventor/Applicant Name:	Takashi Watanabe
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Filer Authorized By:	Michael J. Caridi
Attorney Docket Number:	042341
Receipt Date:	28-OCT-2009
Filing Date:	14-APR-2004
Time Stamp:	15:54:28
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$180
RAM confirmation Number	2264
Deposit Account	
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement (IDS) Filed (SB/08)	042341_IDS_10_28_2009.pdf	819469 5e0364ae044efc3780a0315ae035fae1 9e9	no	4
Warnings:					
Information:					
2	NPL Documents	JPO_2004-013357_10-13-2009. pdf	203702 006a7ee0e278656ba756c0b6c189b1cfbe 408	no	6
Warnings:					
Information:					
3	Foreign Reference	JP_10-303152v2.pdf	2551776 6b4f7714d19132e7f3c2986ce9f8e89c7188 51a	no	15
Warnings:					
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4	Foreign Reference	JP_2000-243733v2.pdf	2296858 4476e273ade3b554f3846e4e550006e9d13 716	no	12
Warnings:					
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5	Foreign Reference	JP_2000-315665v2.pdf	4978378 86c126686dec95c91dd46a72ad4618e6a5 6e45	no	27
Warnings:					
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6	Foreign Reference	JP_2001-57352v2.pdf	1953562 025f6eac696c2c8ba7f6da2049478236c97 632	no	10
Warnings:					
Information:					
7	Foreign Reference	JP_2003-142435v2.pdf	1476104 bb4a05c8a079e87963862d8d9e7fe6a276 a37eb	no	10
Warnings:					
Information:					
8	Fee Worksheet (PTO-875)	fee-info.pdf	30007 e0c7b16ed5db96ade0bfcd708db6b30b 9140e	no	2
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